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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/673,208

Docket No.: 107281

Filed: October 13, 2000

For: SEMICONDUCTOR DEVICE, MOUNTING SUBSTRATE AND METHOD OF  
MANUFACTURING MOUNTING SUBSTRATE, CIRCUIT BOARD AND  
ELECTRONIC INSTRUMENT

**REQUEST FOR CORRECTION OF PALM RECORDS**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,



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Date: April 19, 2001

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DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461
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## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/673,208	10/13/2000	2811	2334	107281	12	53	13

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MAR 07 2001

## CORRECTED FILING RECEIPT



\*OC000000005818912\*

Date Mailed: 03/02/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Nobuaki Hashimoto, Nagano-ken, JAPAN;

## Continuing Data as Claimed by Applicant

THIS APPLICATION IS A 371 OF PCT/JP00/00894 02/17/2000

## Foreign Applications

JAPAN 11-039623(P) 02/18/1999

If Required, Foreign Filing License Granted 11/20/2000

## Title

Semiconductor device, mounting <sup>substrate</sup> and method of manufacturing mounting substrate, circuit board, and electronic instrument

## Preliminary Class

257

Data entry by : TOWLER, VERNON

Team : OIPE

Date: 03/02/2001

